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Application Number Filed Herewith Substitute for form 1449A/PTO Filing Date March 11, 2004 INFORMATION DISCLOSURE First Named Inventor: JengPing Lu STATEMENT BY APPLICANT Examiner name: unknown GROUP: unknown (Use as many sheets as necessary) Sheet of 1 Attorney Docket Number A3490 (XCP-040) U.S. PATENT DOCUMENTS Examiner Cited Document Number **Publication Date** Name of Patentee or Pages, Columns, Lines No. 1 Number - Kind Code 2(if known) MM-DD-YYYY Initials* Applicant of Cited Document Where Relevant Passages or Figures Appear A01 US-3,842,189 10/15/1974 Southgate A02 US-5,613,861 03/25/1997 Smith et al. US-US-US-FOREIGN PATENT DOCUMENTS Examiner Publication Cite Pages, Columns, Lines Initials* No. Foreign Patent Document Name of Patentee or Date Where Relevant Applicant of Cited T6 MM-DD-Passages or Figures Cntry Code³ Number⁴ Kind Code⁵ (If known) YYYY Document Appear OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Fonash: "Integration Of Thin Film Electronics And MEMs", SPIE Vol. 2722, 2/96, pp. 66-74. Martin et al.: "High-Voltage Amorphous Silicon Thin-Film Transistors", IEEE Transactions On Electron Devices, Vol. 40, No. 3, March 1993, pp. 634-643. Huang et al.: "Device Sensitivity Of Field-Plated Polysilicon High-Voltage TFT's And Their Application To Low-Voltage Operation", IEEE Electron Device Letters, Vol. 11, November 1990, pp. 541-543. Welsh et al.: "NCAP Displays: Optical Switching And Dielectric Properties", SID 90 Digest, 1990, pp. 220-223. Chuang et al.: "Page-Wide High-Voltage Polysilicon TFT Array For Electronic Printing", SID 90 Digest, 1990, pp. 508-511. **EXAMINER SIGNATURE:** DATE CONSIDERED:

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